



P/1034-54

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Johannes Roelof Gerardus de Vries

Date: March 13, 1997

Serial No.: 08/422,264

Group Art Unit: 2306

Filed: April 14, 1995

Examiner: R. Dolan

For: DATA PROCESSING CIRCUIT, MULTIPLIER UNIT WITH PIPELINE,
ALU AND SHIFT REGISTER UNIT FOR USE IN A DATA PROCESSING
CIRCUIT*Co-mail*
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AMENDMENT TRANSMITTAL LETTER - FEE COMPUTATION

APR 07 1991,

Hon. Commissioner of Patents and Trademarks
Washington, D.C. 20231

GROUP 2300

Transmitted herewith is an amendment in the above-
identified application."Small Entity" status (37 C.F.R. §1.9 & §1.27) established
previously by enclosed verified statement.OFGS Check No. , which includes the fee of \$22.00 calculated
below, is attached.

NO. CLAIMS AFTER AMENDMENT	HIGHEST NO. PREVIOUSLY PAID FOR	EXTRA PRESENT	RATE	ADDIT. FEE
TOTAL 22	MINUS 20	* = 2	x (\$11 SE or \$22)	\$ 22.00
INDEP. 7	MINUS 7	** =	x (\$40 SE or \$80)	\$
FIRST PRESENTATION OF MULTIPLE DEPENDENT CLAIM			x (\$130 SE or \$260)	\$

* not less than 20 ** not less than 3 TOTAL \$ 22.00
In the event the actual fee is greater than the payment submitted
or is inadvertently not enclosed or if any additional fee during
the prosecution of this application is not paid, the Patent Office
is authorized to charge the underpayment to Deposit Account No.
15-0700.

If this communication is filed after the shortened statutory time period had elapsed and no separate Petition is enclosed, the Commissioner of Patents and Trademarks is petitioned, under 37 C.F.R. §1.136(a), to extend the time for filing a response to the outstanding Office Action by the number of months which will avoid abandonment under 37 C.F.R. §1.135. The fee under 37 C.F.R. § 1.17 should be charged to our Deposit Account No. 15-0700.

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, Washington, D.C. 20231, on March 13, 1997:

James A. Finder

Name of Registered Representative

Signature

March 13, 1997

Date of Signature

Respectfully submitted,

James A. Finder

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P/1034-54

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

New York, New York

Johannes Roelof Gerardus de Vries

Date: March 13, 1997

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For: DATA PROCESSING CIRCUIT, MULTIPLIER UNIT WITH PIPELINE,
ALU AND SHIFT REGISTER UNIT FOR USE IN A DATA
PROCESSING CIRCUIT

Assistant Commissioner of Patents
and Trademarks
Washington, D.C. 20231

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260 N.Y.
1403

APR 07 1997

Sir:

GROUP 2300

In response to the Office Action mailed December 18, 1996, please reconsider the above-identified application amended as follows:

IN THE CLAIMS:

Please cancel claims 18 and 20 without prejudice.

Please amend the claims as follows:

5 CI 2/2 sub 2/2 5. (Twice Amended) An arithmetic logic unit having a varying word length for receiving and performing arithmetic operations on integer data words of varying length [comprising a plurality of partitioned arithmetic logic units therein], the word length of the arithmetic logic unit being automatically adjustable in accordance with the length of the integer data words being processed, the length of the integer data words being 8 bits or multiples thereof.

6. (Twice Amended) A shift register unit [having control logic] capable of receiving an integer data word having a length [which is variable in increments] of 8 bits or multiples thereof, the shift register unit for shifting [a 32 bit] the integer data word through a distance of 1 to [32] multiples of 8 bits, in one of a left and a right direction and in one of a rotating and a non-rotating manner.